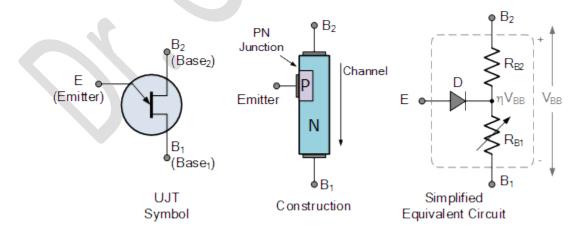
# UNIT-1 MPHYCC-7

## **UJT**

The Unijunction Transistor or UJT for short, is another solid state three terminal device that can be used in gate pulse, timing circuits and trigger generator applications to switch and control either thyristors and triac's for AC power control type applications. Like diodes, unijunction transistors are constructed from separate P-type and N-type semiconductor materials forming a single (hence its name Uni-Junction) PN-junction within the main conducting N-type channel of the device. Although the Unijunction Transistor has the name of a transistor, its switching characteristics are very different from those of a conventional bipolar or field effect transistor as it can not be used to amplify a signal but instead is used as a ON-OFF switching transistor. UJT's have unidirectional conductivity and negative impedance characteristics acting more like a variable voltage divider during breakdown.

Like N-channel FET's, the UJT consists of a single solid piece of N-type semiconductor material forming the main current carrying channel with its two outer connections marked as Base 2 ( $B_2$ ) and Base 1 ( $B_1$ ). The third connection, confusingly marked as the Emitter (E) is located along the channel. The emitter terminal is represented by an arrow pointing from the P-type emitter to the N-type base. The Emitter rectifying p-n junction of the unijunction transistor is formed by fusing the P-type material into the N-type silicon channel. However, P-channel UJT's with an N-type Emitter terminal are also available but these are little used. The Emitter junction is positioned along the channel so that it is closer to terminal  $B_2$  than  $B_1$ . An arrow is used in the UJT symbol which points towards the base indicating that the Emitter terminal is positive and the silicon bar is negative material. Below shows the symbol, construction, and equivalent circuit of the UJT.

## **Unijunction Transistor Symbol and Construction**



Notice that the symbol for the unijunction transistor looks very similar to that of the junction field effect transistor or JFET, except that it has a bent arrow representing the Emitter (E)

input. While similar in respect of their ohmic channels, JFET's and UJT's operate very differently and should not be confused.

So how does it work? We can see from the equivalent circuit above, that the N-type channel basically consists of two resistors  $R_{B2}$  and  $R_{B1}$  in series with an equivalent (ideal) diode, D representing the p-n junction connected to their center point. This Emitter p-n junction is fixed in position along the ohmic channel during manufacture and can therefore not be changed. Resistance  $R_{B1}$  is given between the Emitter, E and terminal  $B_1$ , while resistance  $R_{B2}$  is given between the Emitter, E and terminal  $B_1$ , while resistance  $R_{B2}$  is given between the Emitter, E and terminal  $B_2$ . As the physical position of the p-n junction is closer to terminal  $B_2$  than  $B_1$  the resistive value of  $R_{B2}$  will be less than  $R_{B1}$ . The total resistance of the silicon bar (its Ohmic resistance) will be dependent upon the semiconductors actual doping level as well as the physical dimensions of the N-type silicon channel but can be represented by  $R_{BB}$ .

These two series resistances produce a voltage divider network between the two base terminals of the unijunction transistor and since this channel stretches from  $B_2$  to  $B_1$ , when a voltage is applied across the device, the potential at any point along the channel will be in proportion to its position between terminals  $B_2$  and  $B_1$ . The level of the voltage gradient therefore depends upon the amount of supply voltage. When used in a circuit, terminal  $B_1$  is connected to ground and the Emitter serves as the input to the device. Suppose a voltage  $V_{BB}$  is applied across the UJT between  $B_2$  and  $B_1$  so that  $B_2$  is biased positive relative to  $B_1$ . With zero Emitter input applied, the voltage developed across  $R_{B1}$  (the lower resistance) of the resistive voltage divider can be calculated as:

### Unijunction Transistor R<sub>B1</sub> Voltage

$$V_{RB1} = \frac{R_{B1}}{R_{B1} \pm R_{B2}} \times V_{BE}$$

For a unijunction transistor, the resistive ratio of  $R_{B1}$  to  $R_{BB}$  shown above is called *the intrinsic stand-off ratio and is given the Greek symbol:*  $\eta$  *(eta)*. Typical standard values of  $\eta$  range from 0.5 to 0.8 for most common UJT's.

If a small positive input voltage which is less than the voltage developed across resistance,  $R_{B1}$  ( $\eta VBB$ ) is now applied to the Emitter input terminal, the diode p-n junction is reverse biased, thus offering a very high impedance and the device does not conduct. The UJT is switched "OFF" and zero current flows. However, when the Emitter input voltage is increased and becomes greater than  $V_{RB1}$  (or  $\eta V_{BB} + 0.7V$ , where 0.7V equals the p-n junction diode volt drop) the p-n junction becomes forward biased and the unijunction transistor begins to conduct. The result is that Emitter current,  $\eta IE$  now flows from the Emitter into the Base region. The effect of the additional Emitter junction and the B<sub>1</sub> terminal. This reduction in the value of R<sub>B1</sub> resistance to a very low value means that the Emitter junction becomes even more forward biased resulting in a larger current flow. The effect of this results in a negative resistance at the Emitter terminal.

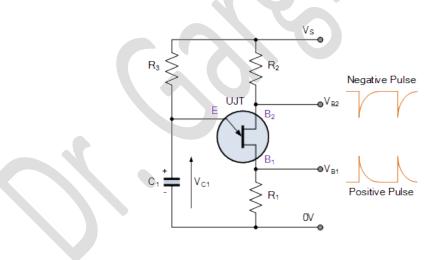
Likewise, if the input voltage applied between the Emitter and  $B_1$  terminal decreases to a value below breakdown, the resistive value of  $R_{B1}$  increases to a high value. Then the Unijunction Transistor can be thought of as a voltage breakdown device.

So we can see that the resistance presented by  $R_{B1}$  is variable and is dependent on the value of Emitter current, IE. Then forward biasing the Emitter junction with respect to  $B_1$  causes more current to flow which reduces the resistance between the Emitter, E and  $B_1$ . In other words, the flow of current into the UJT's Emitter causes the resistive value of  $R_{B1}$  to decrease and the voltage drop across it,  $V_{RB1}$  must also decrease, allowing more current to flow producing a negative resistance condition.

## **Unijunction Transistor Applications**

Now that we know how a *unijunction transistor* works, what can they be used for. The most common application of a unijunction transistor is as a triggering device for *SCR*'s and *Triacs* but other UJT applications include sawtoothed generators, simple oscillators, phase control, and timing circuits. The simplest of all UJT circuits is the Relaxation Oscillator producing non-sinusoidal waveforms. In a basic and typical UJT relaxation oscillator circuit, the Emitter terminal of the unijunction transistor is connected to the junction of a series connected resistor and capacitor, RC circuit as shown below.

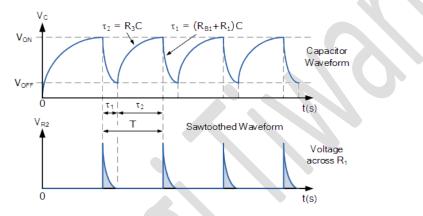
## **Unijunction Transistor Relaxation Oscillator**



When a voltage (Vs) is firstly applied, the unijunction transistor is "OFF" and the capacitor  $C_1$  is fully discharged but begins to charge up exponentially through resistor  $R_3$ . As the Emitter of the UJT is connected to the capacitor, when the charging voltage Vc across the capacitor becomes greater than the diode volt drop value, the p-n junction behaves as a normal diode and becomes forward biased triggering the UJT into conduction. The unijunction transistor is "ON". At this point the Emitter to  $B_1$  impedance collapses as the Emitter goes into a low impedance saturated state with the flow of Emitter current through  $R_1$  taking place. As the ohmic value of resistor  $R_1$  is very low, the capacitor discharges rapidly through the UJT and a fast rising voltage pulse appears across  $R_1$ . Also, because the capacitor discharges more quickly through the UJT than it does charging up through resistor  $R_3$ , the discharging time is a lot less than the charging time as the capacitor discharges through the low resistance UJT.

When the voltage across the capacitor decreases below the holding point of the p-n junction ( $V_{OFF}$ ), the UJT turns "OFF" and no current flows into the Emitter junction so once again the capacitor charges up through resistor  $R_3$  and this charging and discharging process between  $V_{ON}$  and  $V_{OFF}$  is constantly repeated while there is a supply voltage, Vs applied.

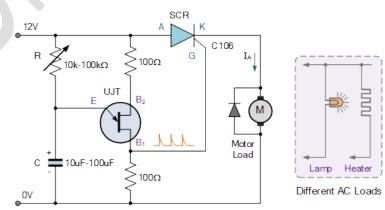
#### **UJT Oscillator Waveforms**



Then we can see that the unijunction oscillator continually switches "ON" and "OFF" without any feedback. The frequency of operation of the oscillator is directly affected by the value of the charging resistance  $R_3$ , in series with the capacitor  $C_1$  and the value of  $\eta$ . The output pulse shape generated from the Base1 (B<sub>1</sub>) terminal is that of a sawtooth waveform and to regulate the time period, you only have to change the ohmic value of resistance,  $R_3$  since it sets the RC time constant for charging the capacitor. The time period, T of the sawtoothed waveform will be given as the charging time plus the discharging time of the capacitor. As the discharge time,  $\tau 1$  is generally very short in comparison to the larger RC charging time,  $\tau 2$  the time period of oscillation is more or less equivalent to  $T \cong \tau 2$ . The frequency of oscillation is therefore given by

$$f = \frac{1}{T}$$

# **Unijunction Transistor Speed Control**



One typical application of the unijunction transistor circuit above is to generate a series of pulses to fire and control a thyristor. By using the UJT as a phase control triggering circuit in conjunction with an SCR or Triac, we can adjust the speed of a universal AC or DC motor as shown.

# **Unijunction Transistor Summary**

We have seen that a Unijunction Transistor or UJT for short, is an electronic semiconductor device that has only one p-n junction within a N-type (or P-type) lightly doped ohmic channel. The UJT has three terminals one labelled Emitter (E) and two Bases (B<sub>1</sub> and B<sub>2</sub>). Two ohmic contacts B<sub>1</sub> and B<sub>2</sub> are attached at each ends of the semiconductor channel with the resistance between B<sub>1</sub> and B<sub>2</sub>, when the emitter is open circuited being called the inter base resistance, R<sub>BB</sub>. If measured with an ohmmeter, this static resistance would typically measure somewhere between about  $4k\Omega$  and  $10k\Omega$ 's for most common UJT's.

The ratio of  $R_{B1}$  to  $R_{BB}$  is called the intrinsic stand-off ratio, and is given the Greek symbol:  $\eta$  (eta). Typical standard values of  $\eta$  range from 0.5 to 0.8 for most common UJT's. The unijunction transistor is a solid state triggering device that can be used in a variety of circuits and applications, ranging from the firing of thyristors and triacs, to the use in sawtooth generators for phase control circuits. The negative resistance characteristic of the UJT also makes it very useful as a simple relaxation oscillator.

When connected as a relaxation oscillator, it can oscillate independently without a tank circuit or complicated RC feedback network. When connected this way, the unijunction transistor is capable of generating a train of pulses of varying duration simply by varying the values of a single capacitor, (C) or resistor, (R).

Commonly available unijunction transistors include the 2N1671, 2N2646, 2N2647, etc, with the 2N2646 being the most popular UJT for use in pulse and sawtooth generators and time delay circuits. Other types of unijunction transistor devices available are called Programmable UJTs, which can have their switching parameters set by external resistors. The most common Programmable Unijunction Transistors are the 2N6027 and the 2N6028.